



Sri SAI RAM INSTITUTE OF TECHNOLOGY

An ISO 9001 : 2008 Certified Institution

Sai Leo Nagar, West Tambaram, Chennai - 44. www.sairamit.edu.in



Name : M.RAZMAH
Designation : Assistant Professor
Email : razmah.eee@sairamit.edu.in

Qualification : M.E
Specialization : Applied Electronics

Research Interest : -

Experience in years : **Teaching** UG 7.10 PG **Industry**

No. of Workshop/Conferences/
FDP attended : Workshop 2 Conferences 1 FDP **6**

No. of Workshop/Conferences/
FDP Organized : Workshop Conferences FDP

Professional Membership	
Publications	National : -
	International:1
	Book : -
Research Funded Projects	-

Patents	-
Achievements	Received Rs. 2,000 Cash Award from Sri Sairam Institute of technology for Producing 100% in “Embedded Systems” Subject in the Year 2017.
Any Other Information	<ol style="list-style-type: none"> 1. Microprocessor and Microcontroller lab Incharge 2. Acted as Class Coordinator for 2013-17 Batch and 2015-2019 batch. 3. Result Analysis Coordinator for 2013-2017 Batch 4. Mentoring the students 5. Acted as Class committee chairman

List of Publications:-

1. **M.Razmah,R.Anitha** “Substation monitoring and Control System using GSM” in **Global Journal of Advanced Engineering Technologies**.Volume 5,Issue 03,ISSN (online): 2277-6370, 2016.

List of Patent: -

Workshop / Conferences / FDP Attended:-

- Attended Three days FDP on ELECTROMAGNETIC THEORY AND CONTROL SYSTEMS organized by Sree Sastha Institute of Engineering and Technology from 19.06.2017 to 21.06.2017.
- Attended Two days FDP on RECENT TRENDS on RENEWABLE ENERGY and POWER ELECTRONICS using MATLAB in association with Pantech ProEd Pvt Ltd at AMET University from 15.09.2017 to 16.09.2017.
- Attended Two days IET Sponsored FDP on POWER SYSTEM ANALYSIS using ETAP at KCG College of Technology from 22.06.2017 to 23.06.2017.
- Attended Two days International Workshop on Power System studies using ETAP and Digsilent in S.A Engg College Chennai from 04.05.2017 to 05.05.2017.
- Attended Seven days Faculty Development Programme on Power Electronics organized by Centre for Faculty Development at Sri Sairam Institute of Technology from 1st to 8th June 2015.

- Attended two days ISTE Sponsored Faculty Development Programme on Electromagnetic Field theory and Transmission Lines at Sri Sairam Institute of Technology on 25th and 26th June 2015.
- Attended two days Faculty Development Programme on Analog System Design using ASLK pro organized in association with Starcom Information Technology Ltd and Texas Instruments (India) University Program at Anand Institute of Higher Technology ,chennai during May 20-21,2015
- Attended one day Workshop on Recent Trends in Renewable energy at Sri Sairam Institute of Technology on 31st March 2015.
- Attended two days National Conference on Communication and Nano Electronics Technology-2011 organized by Sri Sairam Institute of Technology from oct 12 2011 to oct 14 2011.

INTERNATIONAL CONFERENCE:

Presented the paper entitled

1. “Reduction of Transients in PCB Traces”
2. “Crosstalk Minimization in Micro-strip Lines using Electromagnetic Band Gap Structures.
3. “Design of Meander Delay Lines and Near End Crosstalk Analysis (next) in High Speed printed circuit Boards.

Organized by Post Graduate and Research Department of Electronics S.N.R Sons College (Autonomous),Coimbatore -641 006, India and Femto Logic Design – Chennai, India In Association with INTERNATIONAL JOURNAL OF COMPUTER NETWORKS AND COMMUNICATIONS (IJCNC) INTERNATIONAL CONFERENCE ON “SYSTEM ON CHIP “(SoC)”

Workshop / Conferences / FDP Organized:-

- Organized two days Workshop on “**ORCAD –PSPICE**” at Sri Sairam Institute of Technology on Aug 5th and Aug 6th 2015.