



Sri SAI RAM INSTITUTE OF TECHNOLOGY

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Sai Leo Nagar, West Tambaram, Chennai - 44. www.sairamit.edu.in

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Name : R.DHANASEKAR
Designation : Assistant Professor
Email : ghanasekar.eee@sairamit.edu.in
Qualification : M.E., (Ph.D doing)
Specialization : Power Electronics and Drives
Research Interest : Sliding Mode Controller
Experience in years : **Teaching** UG PG **Industry**
No. of Workshop/Conferences/
FDP attended : Workshop Conferences FDP
No. of Workshop/Conferences/
FDP Organized : Workshop Conferences FDP



Professional Membership	Life Member, Indian Society for Technical Education (ISTE), New Delhi,
Publications	National :
	International:7
	Book :
Research Funded Projects	
Patents	

Achievements	Cash award for 100% result in Power Electronics, Electrical Machines –II, Power quality, Power Plant Engineering, Basic Electrical & Electronics Engineering
Any Other Information	

List of Publications:-

1. Published a paper titled “Simultaneous Ac and Dc Output Using Hybrid Converter Modeling” in International Journals of Power Electronics Controllers and Converters, Oct 2015, ISSN: 2456-1614.
2. Published a paper titled “Ecofriendly hybrid E Vehicle” in International Journal Of Multidisciplinary Research Centre (IJMRC), Sep 2015, ISSN:2454-3861.
3. Published a paper titled “Power generation from microbial fuel cell” in International Journal of Analog Integrated Circuits, Oct 2015, ISSN: 0976-5697.
4. Published a paper titled “Bidirectional intelligent semiconductor transformer” in Journal of Engineering and Fundamentals, Dec 2015, 2321-5933
5. Published a paper titled “Sliding mode control of electric drives/review” in IEEE XPLORE, DEC 2016, ISBN: 978-1-5090-1147-6.
6. Published a paper titled “Microinverter With High Efficiency Snubber Circuit For A Grid Interactive PV Panel” in International Journal of Creative research Thoughts(IJCRT) - UGC Approved - Volume 6, Issue 1 January 2018 | ISSN: 2320-2882.
7. Published a paper titled “A Solar Powered Water Pumping Framework Using Switched Reluctance Motor” in International Journal of Creative research Thoughts(IJCRT) - UGC Approved - Volume 6, Issue 1 January 2018 | ISSN: 2320-2882.

List of Patent:

1.NIL

Workshop / Conferences / FDP Attended:-

1. Attended one day National Workshop on “**Embedded Technologies in Special Machines**” held on 07.02.2012 at Easwari Engineering College.
2. Attended one day Workshop on “**Recent Trends in Renewable Energy**” held on 31.03.2015 at Sri Sairam Institute of Technology.
3. Attended 7th International conference on Informatics and computing in Engineering Systems- ICICES 2018 with paper titled “**Development of mobile car black box for Accident and reporting embedded technology with synchronous action of vehicle control unit and Android mobile**” in SA Engineering College on 21 and 22nd March - 2018.
4. Attended 7th International conference on Informatics and computing in Engineering Systems- ICICES 2018 with paper titled “**A Novel cascaded multilevel converter for Battery Energy management system**” in SA Engineering College on 21 and 22nd March - 2018.
5. Attended 7th International conference on Computation of power, Energy, Information and Communication - ICCPEIC 2018 with paper titled “**Improved source end power factor Enhancement of BLDC motor using Bridgeless – Landsman converter**” Adhiparasakthi Engineering College on 28 and 29nd March -2018.
6. Attended 7th International conference on Computation of power, Energy, Information and Communication - ICCPEIC 2018 with paper titled “**Modified Sepic converter based PV Inverter for power quality Improvement**” Adhiparasakthi Engineering College on 28 and 29nd March -2018.
7. Attended International conference on Automatica ICA (IEEE ICS ACCA 2016 Curico, Chile) with paper titled “**Sliding mode control of electric drives/review**” from 19.10.2016 to 21.10.2016.
8. Attended National conference on “Communication and Nano Electronics Technology - 2011” with paper titled “**Utility based joined physical MAC layer optimization in OFDM**” from 12.10.2011 to 14.10.2011.
9. Attended National conference on “Power Electronics and Power Systems, MEPPCON - 2006” with paper titled “**Design simulation and implementation of SPWM based UPS using DSP**” on 03.02.2006 to 04.02.2006.
10. Attended National conference on “**Power Electronics and Drives, PEDC -2006**” with paper titled “**Implementing SPWM based single phase on-line UPS using DSP**” on 08.03.2006.
11. Attended seven days FDP on “**Power Electronics**” held at College of Engineering, Guindy, Anna University from 01.06.2009 to 07.06.2009.
12. Attended two days FDP on “**Electromagnetic field theory & Transmission lines**” held at Sri Sairam Institute of Technology from 25.06.2015 to 26.06.2015.
13. Attended one day Online FDP ON “**Circuit simulation / PCB design Using Target**”

3001 software” with DellSoft Technologies Pvt. Ltd on 31.07.16

14. Attended two days FDP on “**Green and energy efficient buildings with emphasis in Power management” in KCG college of technology**” at KCG College Of Technology on 15.06.2017 & 16.06.2017
15. Attended seven days FDP on “**ELECTRIC ENERGY GENERATION, UTILIZATION & CONSERVATION**” at GKM College Of Engineering & Technology from 27.11.2017 & 04.12.2017

Workshop / Conferences / FDP Organized:-