



# Sri SAI RAM INSTITUTE OF TECHNOLOGY

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Sai Leo Nagar, West Tambaram, Chennai - 44. www.sairamit.edu.in

## DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

Name : L.VIJAYARAJA

Designation : ASST. PROF

Email : vijayaraja.eee@sairamit.edu.in

Qualification : B.E, M.Tech, (Ph.D).,

Specialization : POWER ELECTRONICS AND DRIVES

Research Interest : Multilevel Inverter

Experience in years : **Teaching** UG  PG  **Industry**

No. of Workshop/Conferences/  
FDP attended : Workshop  Conferences  FDP

No. of Workshop/Conferences/  
FDP Organized : Workshop  Conferences  FDP



Professional Membership	-
Publications	National : -
	International: 5
	Book : -
Research Funded Projects	1 – Funded by DST, Govt of India under the scheme “Innovation and Entrepreneurship Development” for the product title “Egensor”
Patents	1 – Applied for the Invention title “EGENSOR”

Achievements	<p>OBTAINED 100% RESULT IN</p> <ul style="list-style-type: none"> <li>• CIRCUIT THEORY</li> <li>• FLEXIBLE AC TRANSMISSION SYSTEM – 3 TIMES</li> <li>• TRANSMISSION AND DISTRIBUTION – 2 TIMES</li> <li>• POWER PLANT ENGINEERING – 3 TIMES</li> <li>• BASIC ELECTRICAL AND ELECTRONICS ENGINEERING</li> <li>• ELECTROMAGNETIC THEORY</li> <li>• POWER ELECTRONICS FOR RENEWABLE ENERGY</li> <li>• POWER SYSTEM TRANSIENTS</li> <li>• MICROCONTROLLER BASED SYSTEM DESIGN</li> </ul>
Any Other Information	<ul style="list-style-type: none"> <li>• OVERALL CO-ORDINATOR OF EXAMCELL</li> <li>• OVERALL CO-ORDINATOR OF HEALTH CLUB</li> <li>• DEPARTMENT INCHARGE FOR “INNOVATION AND ENTREPRENEURSHIP DEVELOPMENT CELL”. ( till ODD SEM 2017 – 2018 since 2016)</li> <li>• Organizing Three Days “ENTREPRENEURSHIP AWARENESS PROGRAM” per year for our department since 2016</li> </ul>

### List of Publications:-

1. Published a paper titled “Simultaneous Ac and Dc Output Using Hybrid Converter Modeling” in International Journals of Power Electronics Controllers and Converter, Vol. 1: Issue 2, JPECC (2015) 1-6.
2. Published a paper titled “Ecofriendly hybrid E Vehicle” in International Journal Of Multidisciplinary Research Centre (IJMRC), ISSN: 2454-3659 (P), Volume I, Issue 4, September 2015
3. Published a paper titled “Power generation from microbial fuel cell” in International Journal of Analog Integrated Circuits, Vol. 1: Issue 2, JAIC (2015) 10-16.
4. Published a paper titled “Bidirectional intelligent semiconductor transformer” in Journal of Engineering and Fundamentals ISSN: 2149- 0325, Vol. 2(2), pp. 9-16, December, 2015
5. Published a paper titled “A Review on Multilevel Inverter with Reduced Switch Count” in IEEE XPLORE ISBN: 978-1-5090-1147-6 DOI: 10.1109/ICA-ACCA.2016.7778467
6. Published a paper titled “Microinverter With High Efficiency Snubber Circuit For A Grid Interactive PV Panel” in International Journal of Creative research Thoughts(IJCRT), Volume 6, Issue 1 January 2018 | ISSN: 2320-2882

7. Published a paper titled A SOLAR POWERED WATER PUMPING FRAMEWORK USING SWITCHED RELUCTANCE MOTOR in International Journal of Creative research Thoughts(IJCRT), Volume 5, Issue 4 December 2017 | ISSN: 2320-2882

### **List of Patent:**

1. Applied a patent under invention title" EGENSOR" Publication Number : 16/2017, Dt 21/04/2017, publication type INA, application number 201741011384.

### **Workshop / Conferences / FDP Attended:-**

1. **ISO Audit Training**, By Creative Management Consultants, At Sri Sairam Institute Of Technology, On 11<sup>th</sup> December 2012.
2. Attended one day Workshop on "**Recent Trends in Renewable Energy**" held on 31.03.2015 at Sri Sairam Institute of Technology.
3. Attended one day FDP on "**A Step Outcome Based Accreditation**" held at Sri Sairam Institute of Technology from 25.06.2015 to 26.06.2015.
4. Attended two days FDP on "**Electromagnetic field theory & Transmission lines**" held at Sri Sairam Institute of Technology from 25.06.2015 to 26.06.2015.
5. Attended one day Online FDP ON "**Circuit simulation / PCB design Using Target 3001 software**" with DellSoft Technologies Pvt. Ltd on 13.07.16
6. Attended International conference on Automatica ICA (IEEE ICS ACCA 2016 Curico, Chile) with paper titled "**A Review on Multilevel Inverter with Reduced Switch Count**" from 19.10.2016 to 21.10.2016.
7. Attended 5 days QIP on "**Pulse Width Modulation technique for VSI**" held at Indian Institute of Science, Bangalore from 21st -25th November 2016.
8. Attended two days FDP on "**POWER SYSTEM ANALYSIS USING ETAP**" at KCG College Of Technology on 22.06.2017 & 23.06.2017.
9. Attended seven days FDP on "**EE6801 – Electric Energy generation, utilization and conservation**" at G.K.M college of engineering and technology from 27.11.2017 to 04.12.2017.
10. Attended International conference on "**International Conference on Informatics Computing in Engineering Systems (ICICES - 2018)**" at S.A Engineering college from 21.03.2018 to 22.03.2018.
11. Attended International conference on "**International Conference on Computing of Power, Energy, Information and Communication (ICCPEIC - 2018)**" at Adhiparasakthi Engineering college from 28.03.2018 to 29.03.2018.

### **Workshop / Conferences / FDP Organized:-**

NIL