



# Sri SAI RAM INSTITUTE OF TECHNOLOGY

An ISO 9001 : 2008 Certified Institution

Sai Leo Nagar, West Tambaram, Chennai - 44. www.sairamit.edu.in

## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING



Name : S.SURENDERANATH

Designation : ASSISTANT PROFESSOR

Email : surenderanath.eee@sairamit.edu.in

Qualification : M.E.

Specialization : APPLIED ELECTRONICS

Research Interest : POWER ELECTRONICS

Experience in years : **Teaching** UG  PG  **Industry**

No. of Workshop/Conferences/  
FDP attended : Workshop  Conferences  FDP

No. of Workshop/Conferences/  
FDP Organized : Workshop  Conferences  FDP

Professional Membership	Life Member, Indian Society for Technical Education (ISTE), New Delhi,
Publications	National :
	International:4
	Book :
Research Funded Projects	Automatic switching of commercial wet grinders

Patents	
Achievements	Cash award for 100% result in Microprocessor and Microcontroller, Linear Integrated Circuits and Applications
Any Other Information	

### List of Publications:-

1. Published a paper titled “Simultaneous DC and 3 phase AC output using BDHC” in International Journals of Applied Engineering Research.
2. Published a paper titled “An Interleaved High Power Flyback Inverter for Standalone Application Using MPPT Algorithm” in International Journal of Adv Research in Dynamical and Control Systems
3. Published a paper titled “Simultaneous DC and three phase output using hybrid converter ” in Journal of Physics
4. Published a paper titled “Emergency automatic signalling system using time scheduling ” in Journal of Physics

### List of Patent :

1.NIL

### Workshop / Conferences / FDP Attended:-

1. Attended TWO DAYS FDP ON "ELECTROMAGNETIC FIELD THEORY & TRANSMISSION LINES" in SRI SAI RAM INSTITUTE OF TECHNOLOGY on 25<sup>th</sup> and 26<sup>th</sup> june 2015
2. Attended 7 DAYS FDTP ON EE6702 - PROTECTION AND SWITCHGEAR IN G.K.M COLLEGE OF ENGINEERING from 22/05/2017 to 29/05/2017.
3. Attended Two days FDP on “RECENT TRENDS ON RENEWABLE ENERGY & POWER ELECTRONICS USING MATLAB”in association with Pantech ProEd Pvt .Ltd at AMET University on 15th & 16th september ,2017

4. Attended Two Days National Workshop On "Effective Writing Of Engineering Journal Paper And Thesis" In Srm University on 16<sup>th</sup> and 17<sup>th</sup> March 2017
5. Attended Two Days Workshop On "Integrated Electrical And Electronics Circuits Training Using Labview" In Vit University Chennai on 28th & 29th April 2017
6. Presented a paper titled "Cascaded Inverter based multilevel STATCOM" in the National conference on IISGSCRE 17 at Velammal Engineering college, on 31<sup>st</sup> March 2017.

**Workshop / Conferences / FDP Organized:-**

1. Organized One day workshop on "Recent Trends in Renewable Energy "at Sri Sai Ram Institute of Technology, Chennai on 31st March 2015.