

## DECLARATION

The above information is true to the best of my knowledge. I agree to abide by the rules and regulation governing the Faculty Development Training Programme. If selected, I shall attend the programme for the entire duration. I also undertake the responsibility to inform the coordinator in advance if I am unable to attend the course.

Date: \_\_\_\_\_  
Place: \_\_\_\_\_ Signature of the Participant

## SPONSORSHIP

Mr. / Ms. / Dr. \_\_\_\_\_ is an employee of our Institution and is hereby sponsored for the above Faculty Development Training Programme. He / She will be permitted to attend the program, if selected.

Signature of the Sponsoring  
Authority with seal

## MAILING ADDRESS

### THE HOD

Department of EEE

Sri Sairam Institute of Technology

Sai Leo Nagar, West Tambaram, Chennai-600 044.

Contact No: 96001 81979, 98940 54749

E-mail id : eeeaufdp16@sairamit.edu.in



## COMMITTEE MEMBERS

### CHIEF PATRON

**Prof. Dr. M. Rajaram**  
Vice-Chancellor, Anna University, Chennai

### PATRON

**Dr. S. Ganesan**  
Registrar, Anna University, Chennai

**Mr. Sai Prakash Leo Muthu**  
Chief Executive Officer  
Sri SaiRam Engineering College

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**Dr. D. Sridharan**, Addl. Director, CFD

### CO CHAIR

**Dr. K. Palanikumar**  
Principal, Sri Sai Ram Institute of Technology

**Mr. A. Anbazhagan**  
HOD/EEE

### CO-ORDINATORS

**Dr. M. Jagadeesh Kumar**  
Professor, Dept of EEE

**Mr. S. Saravanan**  
Assistant Professor, EEE

**Sairam**  
INSTITUTIONS  
www.sairamgroup.in



# SEVEN DAYS FDTP ON EE6008-MICROCONTROLLER BASED SYSTEM DESIGN

(Elective for BE EEE VII Semester)

20.06.2016 to 27.06.2016

## REGISTRATION FORM

### Co-ordinators

**Dr. M. Jagadeesh Kumar, M.E., Ph.D.**

**Mr. S. Saravanan, M.E.**



Approved by  
**ANNA UNIVERSITY**  
Chennai

Organized by  
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING



**Sri SAI RAM INSTITUTE OF TECHNOLOGY**

(An ISO 9001 : 2008 Certified Institution)

Sai Leo Nagar, West Tambaram, Chennai – 44

Phone: 044-22512111  
www.sairamit.edu.in

## THE INSTITUTION

Sri Sai Ram Institute of Technology is an ISO 9001:2008 certified Institution, established by a philanthropist MJF. Ln. Leo Muthu in the year 2008. The college is situated in a natural environment and offers six UG programmes in Engineering and Technology and one PG in the Management studies. The Institution has grown and recognized as one of the reputed Institutions in Tamil Nadu in a short span of time. The Institution promotes academic activities in various levels through conferences, workshops, seminars and so on.

## ABOUT THE DEPARTMENT

The Department of EEE was started in the year 2008 with a clear vision to create ethical and efficient Electrical Engineers with an intention to serve the mankind. So far our department has produced 23 University ranks. The department has achieved 3rd position in Anna University ranking by producing 12 University ranks in the year 2015. The department has played a pioneering role by producing the world class technocrats. The infrastructure and lab facilities are upgraded from time to time and the department also provides adequate opportunities for the students and researchers to learn and innovate.

## ABOUT THE PROGRAMME

To introduce the architecture of PIC microcontroller and to educate the fresh faculty members on use of interrupts, timers, peripheral devices for data communication. Also, to have better understanding about the functional blocks and architecture of ARM processor. Certificates will be issued by the Director, Centre for Faculty Development, Anna University.

## RESOURCE PERSON

Sessions will be handled by experts from leading Institutions and organizations like IIT, Anna University and Industries.

## COURSE COVERAGE

The following topics will be covered:

- PIC Microcontroller, Instruction set and Addressing modes
- Interrupts and Timer
- Interfacing DAC, ADC, and Sensors.
- ARM Processor architecture.
- ARM Processor Pipeline and Applications.

## REGISTRATION DETAILS

- No registration fee for the participants. Admission is restricted only to 25 participants.
- Selection will be based on first come first serve basis.
- The completed application form is to be sent to the address before due dates.
- No TA/DA will be provided, accommodation will be provided only to the outstation participants on prior request and payment basis.
- Priority will be given to young faculty.

## HOW TO APPLY

Fill up the registration form and send by post to the address mentioned. The college will not be responsible any postal delay. The participants can also send the scanned copy of filled in registration form through email.

## IMPORTANT DATES

<b>Submission of Application</b>	:	<b>15.06.2016</b>
<b>Intimation of selection</b>	:	<b>16.06.2016</b>
<b>Confirmation by Participants</b>	:	<b>17.06.2016</b>

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20.06.2016 to 27.06.2016

## REGISTRATION FORM

Name :

Gender : Male / Female

Qualification :

Designation :

Department :

Institution :

Address :

Mobile number :

E-mail ID :

Accommodation Required : Yes / No